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- ☐ 1. **PLAPACK Parallel Linear Algebra Package Design Overview**  
Alpatov, P.; Baker, G.; Edwards, H.C.; Gunnels, J.; Morrow, G.; Overfelt, J.; van de Geijn, R.;  
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- ☐ 2. **An Overview of the BlueGene/L Supercomputer**  
Adiga, N.R.; Almasi, G.; Almasi, G.S.; Aridor, Y.; Barik, R.; Beece, D.; Bellofatto, R.; Bhanot, G.;  
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- ☐ 3. **Large-Scale First-Principles Molecular Dynamics simulations on the BlueGene/L Platform using the Qbox code**  
Gygi, F.; Yates, R.K.; Lorenz, J.; Draeger, E.W.; Franchetti, F.; Ueberhuber, C.W.; de Supinski,  
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- ☐ 4. **A high-performance SIMD floating point unit for BlueGene/L: architecture, compilation, and algorithm design**  
Bachega, L.; Siddhartha Chatterjee; Dockser, K.A.; Gunnels, J.A.; Manish Gupta; Gustavson,

F.G.; Lapkowski, C.A.; Liu, G.K.; Mendell, M.P.; Wait, C.D.; Ward, T.J.C.;  
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5. **Unlocking the Performance of the BlueGene/L Supercomputer**  
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Brayton, R.K.; Gustavson, F.G.; Hachtel, G.D.;  
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Hachtel, G.; Brayton, R.; Gustavson, F.;  
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- ☐ 3. Fast algorithms for rational Hermite approximation and solution of Toeplitz systems  
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- ☐ 4. An Overview of the BlueGene/L Supercomputer  
Adiga, N.R.; Almasi, G.; Almasi, G.S.; Aridor, Y.; Barik, R.; Beece, D.; Bellofatto, R.; Bhanot, G.; Bickford, R.; Blumrich, M.; Bright, A.A.; Brunheroto, J.; Cascaval, C.; Castanos, J.; Chan, W.; Ceze, L.; Coteus, P.; Chatterjee, S.; Chen, D.; Chiu, G.; Cipolla, T.M.; Crumley, P.; Desai, K.M.; Deutsch, A.; Domany, T.; Dombrowa, M.B.; Donath, W.; Eleftheriou, M.; Erway, C.; Esch, J.; Fitch, B.; Gagliano, J.; Gara, A.; Garg, R.; Germain, R.; Giampapa, M.E.; Gopalsamy, B.; Gunnels, J.; Gupta, M.; Gustavson, F.; Hall, S.; Haring, R.A.; Heidel, D.; Heidelberg, P.; Herger, L.M.; Hoenicke, D.; Jackson, R.D.; Jamal-Eddine, T.; Kopcsay, G.V.; Krevat, E.; Kurhekar, M.P.; Lanzetta, A.P.; Lieber, D.; Liu, L.K.; Lu, M.; Mendell, M.; Misra, A.; Moatti, Y.; Mok, L.; Moreira, J.E.; Nathanson, B.J.; Newton, M.; Ohmacht, M.; Oliner, A.; Pandit, V.; Pudota, R.B.; Rand, R.; Regan, R.; Rubin, B.; Ruehli, A.; Rus, S.; Sahoo, R.K.; Sanomiya, A.; Schenfeld, E.; Sharma, M.; Shmueli, E.; Singh, S.; Song, P.; Srinivasan, V.; Steinmacher-Burow, B.D.; Strauss, K.; Surovic, C.; Swetz, R.; Takken, T.; Tremaine, R.B.; Tsao, M.; Umamaheshwaran, A.R.; Verma, P.; Vranas, P.; Ward, T.J.C.; Wazlowski, M.; Barrett, W.; Engel, C.; Drehmel, B.; Hilgart, B.; Hill, D.; Kasemkhani, F.; Krolak, D.; Li, C.T.; Liebsch, T.; Marcella, J.; Muff, A.; Okomo, A.; Rouse, M.; Schram, A.; Tubbs, M.; Ulsh, G.; Wait, C.; Wittrup, J.; Bae, M.; Dockser, K.; Kissel, L.; Seager, M.K.; Vetter, J.S.; Yates, K.;  
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5. A high-performance SIMD floating point unit for BlueGene/L: architecture, compilation, and algorithm design  
 Bachega, L.; Siddhartha Chatterjee; Dockser, K.A.; Gunnels, J.A.; Manish Gupta; Gustavson, F.G.; Lapkowski, C.A.; Liu, G.K.; Mendell, M.P.; Wait, C.D.; Ward, T.J.C.;  
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6. Blue Gene/L, a system-on-a-chip  
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- ┐ 11. An efficient parallel algorithm for the 3-D FFT NAS parallel benchmark  
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- ┐ 12. A high performance parallel algorithm for 1-D FFT  
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- ┐ 13. An active DC link filter for AC-AC converters  
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# Inventor Information for 10/671935

| Inventor Name           | City             | State/Country |
|-------------------------|------------------|---------------|
| GUSTAVSON, FRED GEHRUNG | BRIARCLIFF MANOR | NEW YORK      |
| GUNNELS, JOHN A.        | MT. KISCO        | NEW YORK      |

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## Inventor Name Search Result

Your Search was:

Last Name = GUSTAVSON

First Name = FRED

| Application#             | Patent#                 | Status | Date Filed | Title  | Inventor Name      |
|--------------------------|-------------------------|--------|------------|--|--------------------|
| <a href="#">09918814</a> | <a href="#">6842765</a> | 150    | 07/31/2001 | PROCESSOR DESIGN FOR EXTENDED-PRECISION ARITHMETIC   | GUSTAVSON, FRED G. |
| <a href="#">08217533</a> | Not Issued              | 161    | 03/24/1994 | NUMERICALLY INTENSIVE COMPUTER ACCELERATOR   | GUSTAVSON, FRED G. |
| <a href="#">08313970</a> | <a href="#">5758176</a> | 250    | 09/28/1994 | METHOD AND SYSTEM FOR PROVIDING A SINGLE-INSTRUCTION, MULTIPLE-DATA EXECUTION UNIT FOR PERFORMING SINGLE-INSTRUCTION, MULTIPLE-DATA OPERATIONS WITHIN A SUPERSCALAR DATA PROCESSING SYSTEM | GUSTAVSON, FRED G. |
| <a href="#">08313971</a> | <a href="#">5513366</a> | 150    | 09/28/1994 | METHOD AND SYSTEM FOR DYNAMICALLY RECONFIGURING A REGISTER FILE IN A VECTOR PROCESSOR  | GUSTAVSON, FRED G. |
| <a href="#">08354402</a> | <a href="#">5563818</a> | 150    | 12/12/1994 | A METHOD AND SYSTEM FOR PERFORMING FLOATING-POINT DIVISION USING SELECTED APPROXIMATION VALUES   | GUSTAVSON, FRED G. |
| <a href="#">08368171</a> | Not Issued              | 166    | 01/04/1995 | METHOD AND SYSTEM FOR ADDRESSING REGISTERS IN A DATA PROCESSING UNIT IN AN INDEXED ADDRESSING MODE   | GUSTAVSON, FRED G. |
| <a href="#">08368172</a> | <a href="#">5680338</a> | 250    | 01/04/1995 | METHOD AND SYSTEM FOR VECTOR PROCESSING UTILIZING SELECTED VECTOR ELEMENTS   | GUSTAVSON, FRED G. |
| <a href="#">08368173</a> | <a href="#">5887183</a> | 150    | 01/04/1995 | METHOD AND SYSTEM IN A DATA PROCESSING SYSTEM FOR LOADING AND STORING VECTORS IN A PLURALITY OF MODES  | GUSTAVSON, FRED G. |
| <a href="#">08368176</a> | <a href="#">5890222</a> | 150    | 01/04/1995 | METHOD AND SYSTEM FOR ADDRESSING REGISTERS IN A DATA PROCESSING UNIT IN AN INDIRECT ADDRESSING MODE  | GUSTAVSON, FRED G. |
| <a href="#">08589770</a> | <a href="#">5751619</a> | 250    | 01/22/1996 | RECURRENT ADRITHMETICAL COMPUTATION USING CARRY-SAVE ARITHMETIC  | GUSTAVSON, FRED G. |

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|-----------------|----------------|-----|------------|---|----------------------------|
| <u>08619456</u> | <u>5825677</u> | 250 | 03/20/1996 | NUMERICALLY INTENSIVE<br>COMPUTER ACCELERATOR   | GUSTAVSON, FRED G.         |
| <u>08687825</u> | <u>5832533</u> | 250 | 07/26/1996 | METHOD AND SYSTEM FOR<br>ADDRESSING REGISTERS IN A DATA<br>PROCESSING UNIT IN AN INDEXED<br>ADDRESSING MODE   | GUSTAVSON, FRED G.         |
| <u>10671887</u> | Not<br>Issued  | 71  | 09/29/2003 | Method and structure for producing high<br>performance linear algebra routines using<br>composite blocking based on L1 cache size                                 | GUSTAVSON, FRED<br>GEHRUNG |
| <u>10671888</u> | Not<br>Issued  | 71  | 09/29/2003 | Method and structure for producing high<br>performance linear algebra routines using<br>register block data format routines                                       | GUSTAVSON, FRED<br>GEHRUNG |
| <u>10671889</u> | Not<br>Issued  | 41  | 09/29/2003 | Method and structure for producing high<br>performance linear algebra routines using<br>level 3 prefetching for kernel routines                                   | GUSTAVSON, FRED<br>GEHRUNG |
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| <u>10671935</u> | Not<br>Issued  | 30  | 09/29/2003 | Method and structure for producing high<br>performance linear algebra routines using<br>a selectable one of six possible level 3 L1<br>kernel routines            | GUSTAVSON, FRED<br>GEHRUNG |
| <u>10671937</u> | Not<br>Issued  | 41  | 09/29/2003 | Method and structure for producing high<br>performance linear algebra routines using<br>preloading of floating point registers                                    | GUSTAVSON, FRED<br>GEHRUNG |
| <u>11035902</u> | Not<br>Issued  | 30  | 01/14/2005 | Method and structure for a generalized<br>cache-register file interface with data<br>restructuring methods for multiple cache<br>levels and hardware pre-fetching | GUSTAVSON, FRED<br>GEHRUNG |
| <u>11035933</u> | Not<br>Issued  | 30  | 01/14/2005 | Method and structure for cache aware<br>transposition via rectangular subsections   | GUSTAVSON, FRED<br>GEHRUNG |
| <u>11039907</u> | Not<br>Issued  | 30  | 01/24/2005 | Method and structure for algorithmic<br>overlap in parallel processing for<br>exploitation when load imbalance is<br>dynamic and predictable                      | GUSTAVSON, FRED<br>GEHRUNG |
| <u>11045354</u> | Not<br>Issued  | 25  | 01/31/2005 | Method and structure for a hybrid full-<br>packed storage format as a single<br>rectangular format data structure   | GUSTAVSON, FRED<br>GEHRUNG |
| <u>11050945</u> | Not<br>Issued  | 30  | 02/07/2005 | System and method for detecting a faulty<br>object in a system  | GUSTAVSON, FRED<br>GEHRUNG |
| <u>11052877</u> | Not<br>Issued  | 71  | 02/09/2005 | System and method for algorithmic cache-<br>bypass  | GUSTAVSON, FRED<br>GEHRUNG |
| <u>11133254</u> | Not<br>Issued  | 25  | 05/20/2005 | Method and structure for improving<br>processing efficiency in parallel processing<br>machines for rectangular and triangular                                     | GUSTAVSON, FRED<br>GEHRUNG |

|                 |               |    |            |   |                            |
|-----------------|---------------|----|------------|---|----------------------------|
|                 |               |    |            | matrix routines   |                            |
| <u>11328344</u> | Not<br>Issued | 30 | 01/09/2006 | Method and structure for an improved data<br>reformatting procedure | GUSTAVSON, FRED<br>GEHRUNG |

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Last Name = GUNNELS

First Name = JOHN

| Application#             | Patent#    | Status | Date Filed | Title  | Inventor Name    |
|--------------------------|------------|--------|------------|--|------------------|
| <a href="#">10671887</a> | Not Issued | 71     | 09/29/2003 | Method and structure for producing high performance linear algebra routines using composite blocking based on L1 cache size                              | GUNNELS, JOHN A. |
| <a href="#">10671888</a> | Not Issued | 71     | 09/29/2003 | Method and structure for producing high performance linear algebra routines using register block data format routines                                    | GUNNELS, JOHN A. |
| <a href="#">10671889</a> | Not Issued | 41     | 09/29/2003 | Method and structure for producing high performance linear algebra routines using level 3 prefetching for kernel routines                                | GUNNELS, JOHN A. |
| <a href="#">10671933</a> | Not Issued | 71     | 09/29/2003 | Method and structure for producing high performance linear algebra routines using a hybrid full-packed storage format                                    | GUNNELS, JOHN A. |
| <a href="#">10671934</a> | Not Issued | 71     | 09/29/2003 | Method and structure for producing high performance linear algebra routines using streaming  | GUNNELS, JOHN A. |
| <a href="#">10671935</a> | Not Issued | 30     | 09/29/2003 | Method and structure for producing high performance linear algebra routines using a selectable one of six possible level 3 L1 kernel routines            | GUNNELS, JOHN A. |
| <a href="#">10671937</a> | Not Issued | 41     | 09/29/2003 | Method and structure for producing high performance linear algebra routines using preloading of floating point registers                                 | GUNNELS, JOHN A. |
| <a href="#">11035902</a> | Not Issued | 30     | 01/14/2005 | Method and structure for a generalized cache-register file interface with data restructuring methods for multiple cache levels and hardware pre-fetching | GUNNELS, JOHN A. |
| <a href="#">11035933</a> | Not Issued | 30     | 01/14/2005 | Method and structure for cache aware transposition via rectangular subsections   | GUNNELS, JOHN A. |
| <a href="#">11039907</a> | Not Issued | 30     | 01/24/2005 | Method and structure for algorithmic overlap in parallel processing for exploitation when load imbalance is dynamic and predictable                      | GUNNELS, JOHN A. |
| <a href="#">11041935</a> | Not Issued | 41     | 01/26/2005 | Method and structure for high-performance linear algebra in the presence of limited outstanding miss slots   | GUNNELS, JOHN A. |
| <a href="#">11045354</a> | Not Issued | 25     | 01/31/2005 | Method and structure for a hybrid full-packed storage format as a single   | GUNNELS, JOHN A. |

|                          |            |     |            |   |                  |
|--------------------------|------------|-----|------------|---|------------------|
|                          |            |     |            | rectangular format data structure   |                  |
| <a href="#">11050945</a> | Not Issued | 30  | 02/07/2005 | System and method for detecting a faulty object in a system   | GUNNELS, JOHN A. |
| <a href="#">11052216</a> | Not Issued | 41  | 02/08/2005 | Method and structure for skewed block-cyclic distribution of lower-dimensional data arrays in higher-dimensional processor grids        | GUNNELS, JOHN A. |
| <a href="#">11052877</a> | Not Issued | 71  | 02/09/2005 | System and method for algorithmic cache-bypass  | GUNNELS, JOHN A. |
| <a href="#">11133254</a> | Not Issued | 25  | 05/20/2005 | Method and structure for improving processing efficiency in parallel processing machines for rectangular and triangular matrix routines | GUNNELS, JOHN A. |
| <a href="#">11328344</a> | Not Issued | 30  | 01/09/2006 | Method and structure for an improved data reformatting procedure  | GUNNELS, JOHN A. |
| <a href="#">60377710</a> | Not Issued | 159 | 05/03/2002 | System and method for performing linear algebra   | GUNNELS, JOHN A. |

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